

## P-Channel Enhancement Mode Power MOSFET

<p><b>Description</b></p> <p>The G2K8P15S uses advanced trench technology to provide excellent <math>R_{DS(ON)}</math>, low gate charge. It can be used in a wide variety of applications.</p> <p><b>General Features</b></p> <ul style="list-style-type: none"> <li>● <math>V_{DS}</math> -150V</li> <li>● <math>I_D</math> (at <math>V_{GS} = -10V</math>) -2.2A</li> <li>● <math>R_{DS(ON)}</math> (at <math>V_{GS} = -10V</math>) &lt; 310mΩ</li> <li>● 100% Avalanche Tested</li> <li>● RoHS Compliant</li> </ul> <p><b>Application</b></p> <ul style="list-style-type: none"> <li>● Power switch</li> <li>● DC/DC converters</li> </ul>	<p>Schematic diagram</p> <p>pin assignment</p> <p>SOP-8</p>
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### Ordering Information

Device	Package	Marking	Packaging
G2K8P15S	SOP-8	G2K8P15	4000pcs/Reel

### Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	-150	V
Continuous Drain Current	$I_D$	-2.2	A
Pulsed Drain Current (note1)	$I_{DM}$	-8.8	A
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Power Dissipation	$P_D$	2.5	W
Single pulse avalanche energy (note2)	$E_{AS}$	25	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 To 150	°C

### Thermal Resistance

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	50	°C/W

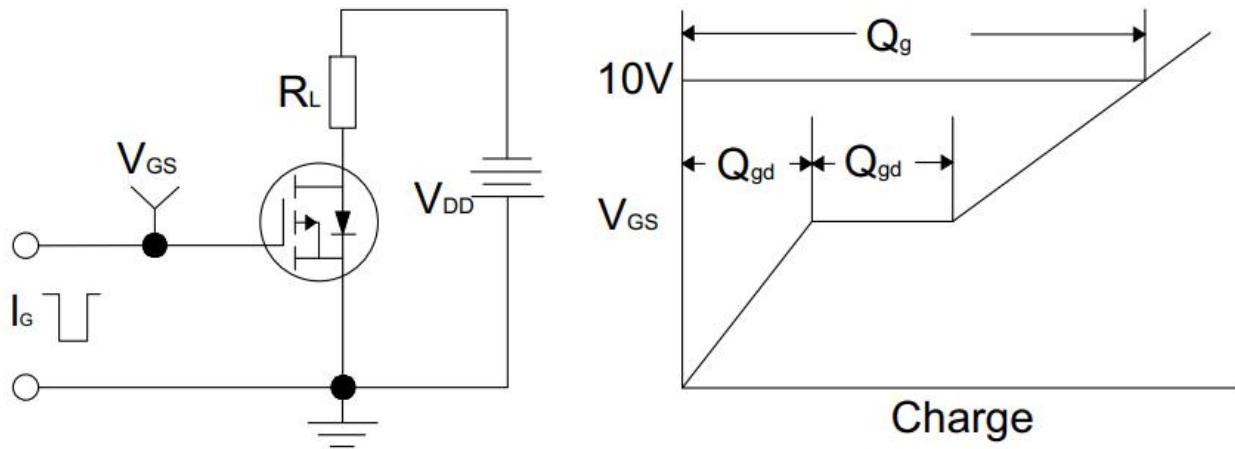
**Specifications**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
<b>Static Parameters</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = -250\mu\text{A}$	-150	--	--	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = -150\text{V}, V_{\text{GS}} = 0\text{V}$	--	--	-1	$\mu\text{A}$
Gate-Source Leakage	$I_{\text{GSS}}$	$V_{\text{GS}} = \pm 20\text{V}$	--	--	$\pm 100$	nA
Gate-Source Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = -250\mu\text{A}$	-1	-2.2	-3.5	V
Drain-Source On-Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = -10\text{V}, I_D = -1\text{A}$	--	277	310	$\text{m}\Omega$
Forward Transconductance	$g_{\text{FS}}$	$V_{\text{DS}} = -15\text{V}, I_D = -1\text{A}$	--	7	--	S
<b>Dynamic Parameters</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = -75\text{V}, f = 1.0\text{MHz}$	--	966	--	pF
Output Capacitance	$C_{\text{oss}}$		--	39	--	
Reverse Transfer Capacitance	$C_{\text{rss}}$		--	24	--	
Total Gate Charge	$Q_g$	$V_{\text{DD}} = -75\text{V}, I_D = -1\text{A}, V_{\text{GS}} = -10\text{V}$	--	11	--	nC
Gate-Source Charge	$Q_{\text{gs}}$		--	3.2	--	
Gate-Drain Charge	$Q_{\text{gd}}$		--	1.9	--	
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = -75\text{V}, I_D = -1\text{A}, R_G = 6\Omega$	--	9.7	--	ns
Turn-on Rise Time	$t_r$		--	2.5	--	
Turn-off Delay Time	$t_{\text{d}(\text{off})}$		--	17	--	
Turn-off Fall Time	$t_f$		--	5.7	--	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Body Diode Current	$I_s$	$T_C = 25^\circ\text{C}$	--	--	-2.2	A
Body Diode Voltage	$V_{\text{SD}}$	$T_J = 25^\circ\text{C}, I_{\text{SD}} = -1\text{A}, V_{\text{GS}} = 0\text{V}$	--	--	-1.2	V
Reverse Recovery Charge	$Q_{\text{rr}}$	$I_F = -1\text{A}, V_{\text{GS}} = 0\text{V}$ $dI/dt = -100\text{A/us}$	--	157	--	nC
Reverse Recovery Time	$T_{\text{rr}}$		--	65	--	ns

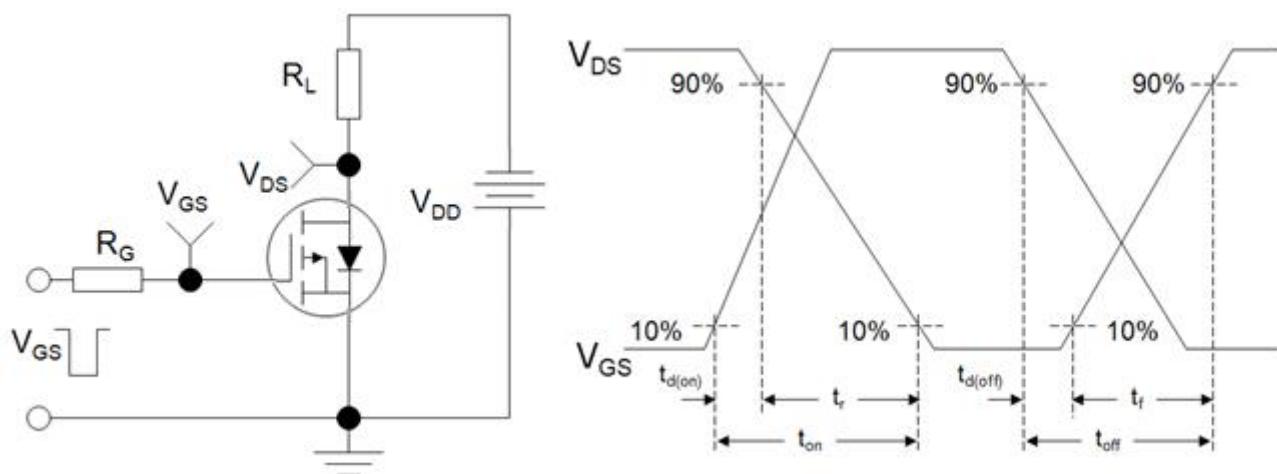
**Notes**

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. EAS condition :  $T_J=25^\circ\text{C}$ ,  $V_{\text{DD}}=-50\text{V}$ ,  $V_{\text{GS}}=-10\text{V}$ ,  $L=0.5\text{mH}$ ,  $R_G=25\Omega$
3. Identical low side and high side switch with identical  $R_G$

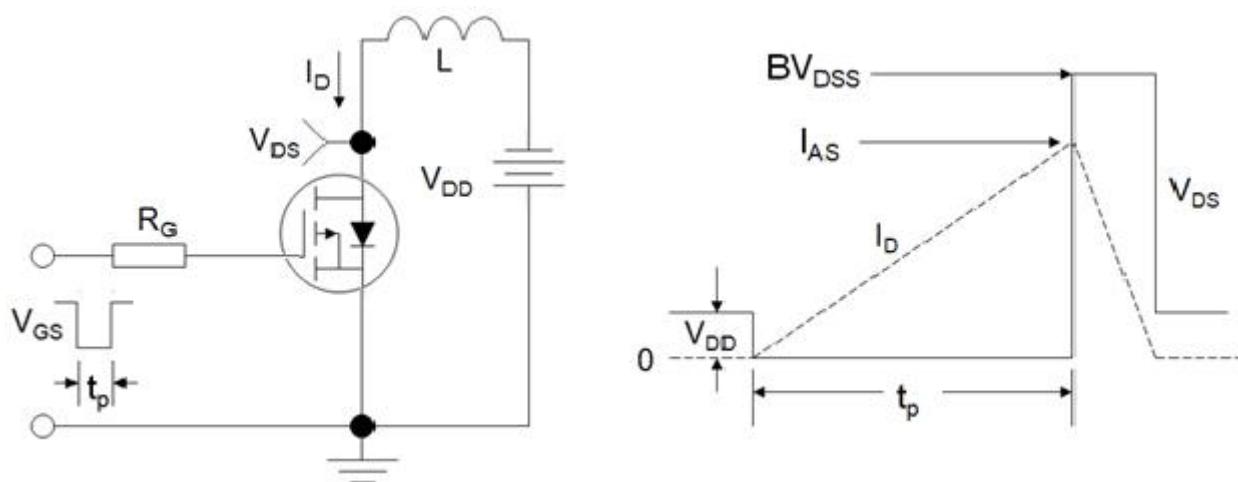
Gate Charge Test Circuit



Switch Time Test Circuit

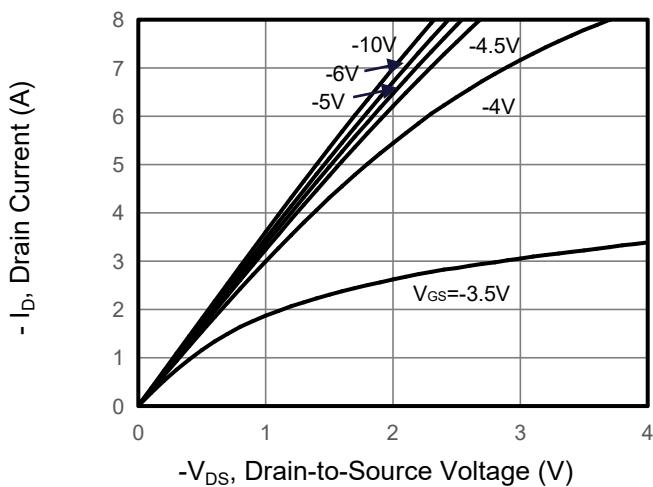


EAS Test Circuit

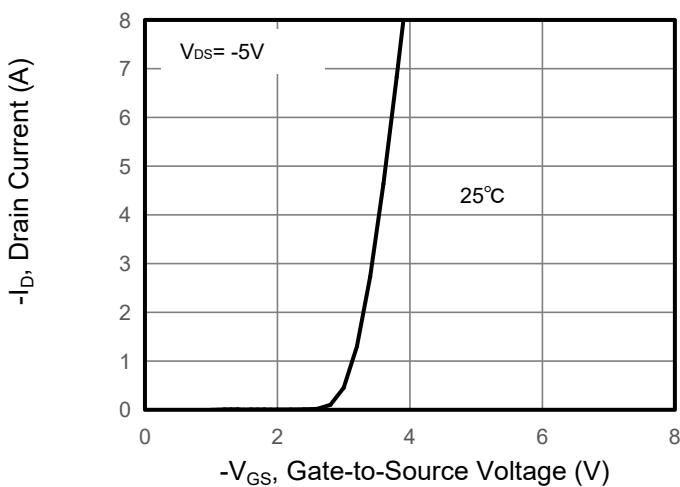


**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

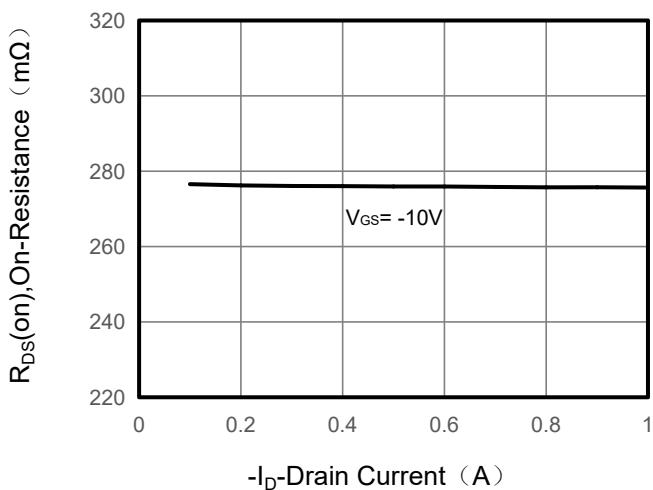
**Figure 1. Output Characteristics**



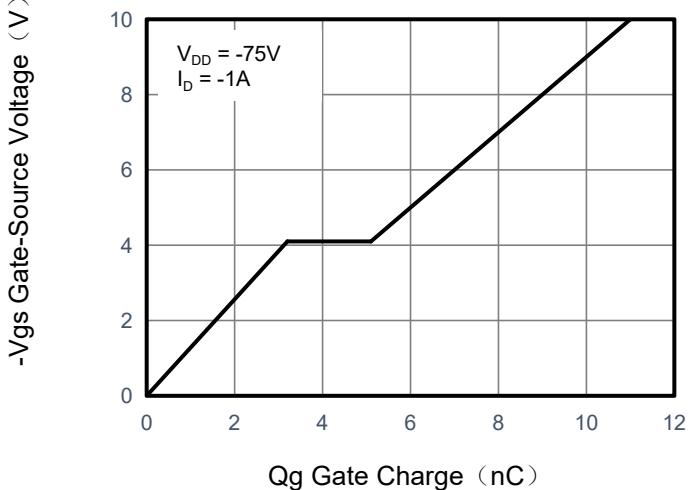
**Figure 2. Transfer Characteristics**



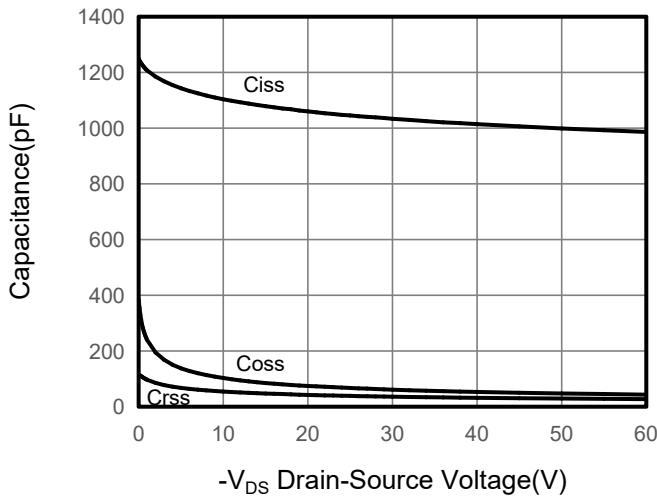
**Figure 3. Drain Source On Resistance**



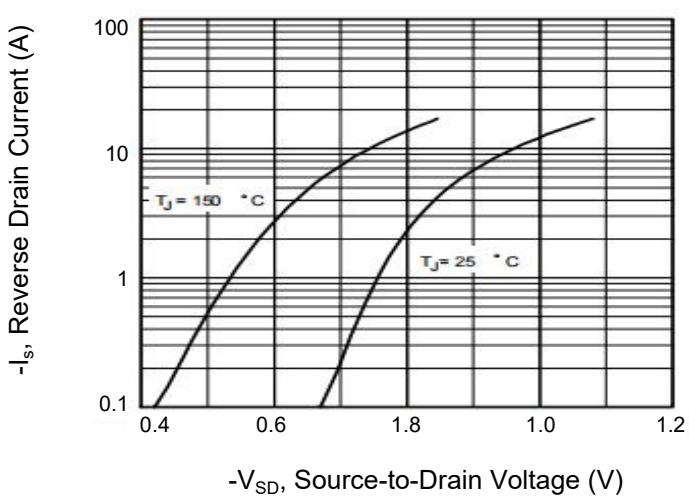
**Figure 4. Gate Charge**



**Figure 5. Capacitance**

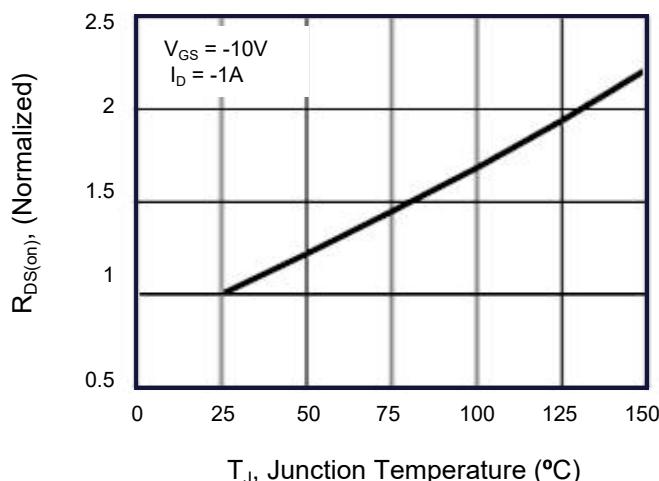


**Figure 6. Source-Drain Diode Forward**

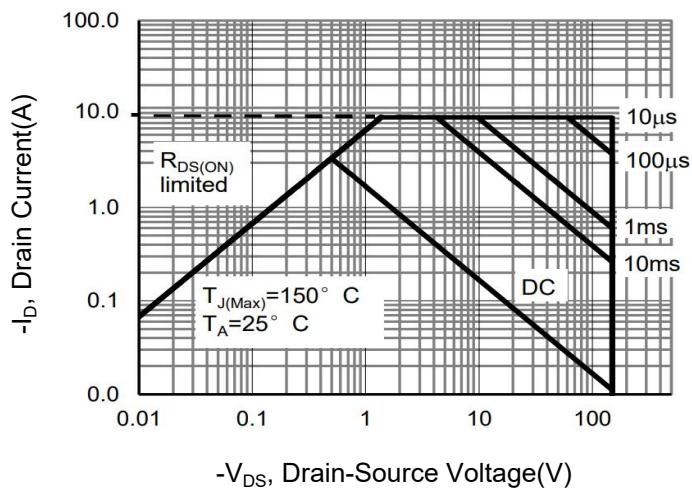


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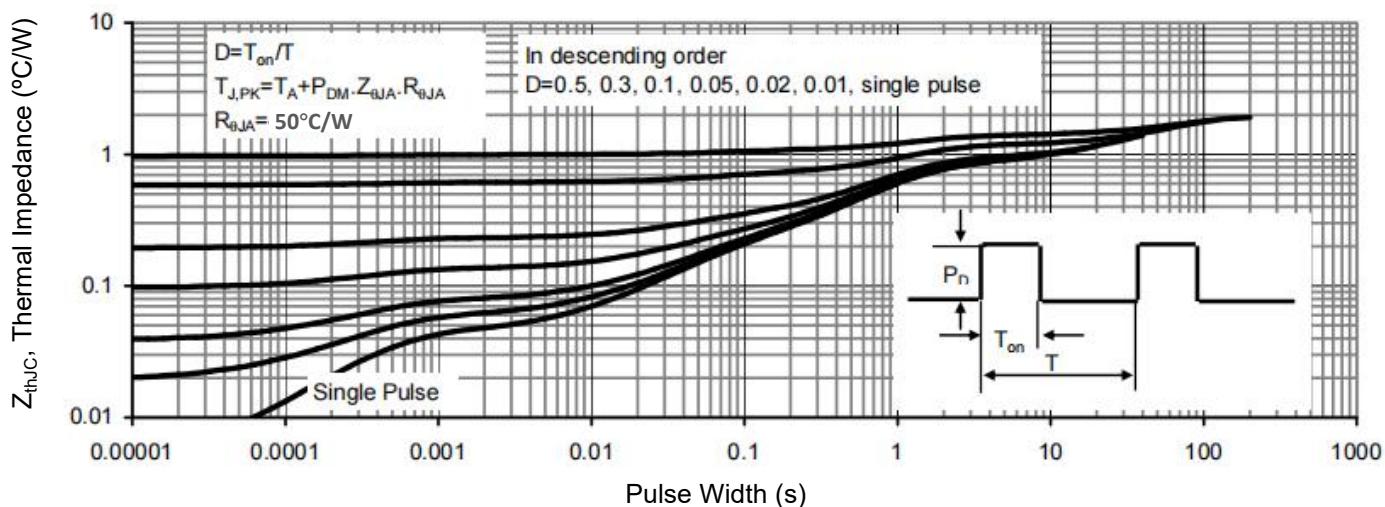
**Figure 7. Drain-Source On-Resistance**

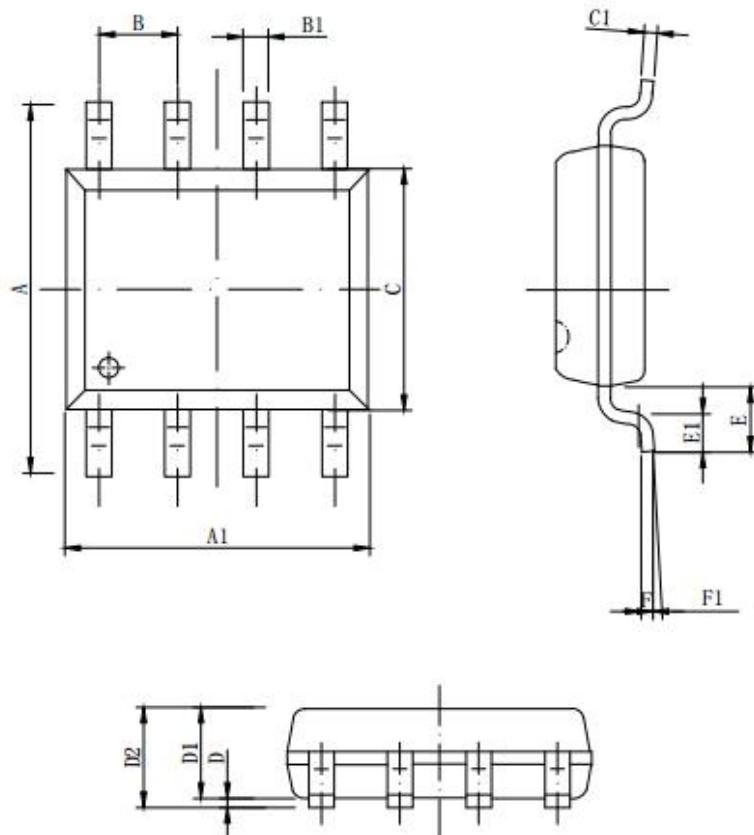


**Figure 8. Safe Operation Area**



**Figure 9. Normalized Maximum Transient Thermal Impedance**



**SOP-8 Package information**

Symbol	Dimensions in Millimeters		
	MIN.	NOM.	MAX.
A	5.800	6.000	6.200
A1	4.800	4.900	5.000
B	1.270BSC		
B1	0.35^8x	0.40^8x	0.45^8x
C	3.780	3.880	3.980
C1	--	0.203	0.253
D	0.050	0.150	0.250
D1	1.350	1.450	1.550
D2	1.500	1.600	1.700
D2	1.500	1.600	1.700
E	1.060REF		
E1	0.400	0.700	0.100
F	0.250BSC		
F1	2°	4°	6°