



100V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BVDSS	R _{DS(ON)} Max	I _D max T _A = +25°C
	$25m\Omega$ @ $V_{GS} = 10V$	7.1A
100V	$33m\Omega$ @ $V_{GS} = 6V$	6.2A
	45mΩ @ V _{GS} = 4.5V	5.3A

Features and Benefits

- 100% Unclamped Inductive Switching Ensures More Reliable and Robust End Application
- High Conversion Efficiency
- Low R_{DS(ON)} Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

https://www.diodes.com/quality/product-definitions/

Description and Applications

This MOSFET is designed to minimize the on-state resistance (RDS(ON)), yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- High Frequency Switching
- Synchronous Rectification
- DC-DC Converters

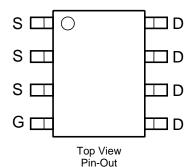
Mechanical Data

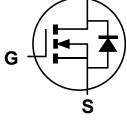
- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 3 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 ©3
- Weight: 0.074 grams (Approximate)



SO-8







Equivalent Circuit

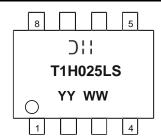
Ordering Information (Note 4)

Part Number	Case	Packaging
DMT10H025LSS-13	SO-8	2,500/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



⊃;; = Manufacturer's Marking T1H025LS = Product Type Marking Code YYWW = Date Code Marking YY or YY = Year (ex: 19 = 2019) WW = Week (01 to 53)



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V_{DSS}	100	V
Gate-Source Voltage		Vgss	±20	V
Continuous Drain Current (Note 6) V _{GS} = 10V	T _A = +25°C T _A = +70°C	I _D	7.1 5.7	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	60	Α	
Maximum Continuous Body Diode Forward Current (Note 6)	Is	10.5	Α	
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)		Ism	60	Α
Avalanche Current, L = 0.3mH		las	8.8	Α
Avalanche Energy, L = 0.3mH		Eas	11.5	mJ

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	PD	1.3	W
Thermal Resistance, Junction to Ambient (Note 5)	RθJA	93	°C/W	
Total Power Dissipation (Note 6)	P_{D}	1.9	W	
Thermal Resistance, Junction to Ambient (Note 6)	Reja	65.7	°C/W	
Total Power Dissipation (Note 6)	Tc = +25°C	PD	12.9	W
Thermal Resistance, Junction to Case (Note 6)	Rejc	9.7	°C/W	
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Electrical Characteristics (T_A = +25°C, unless otherwise specified.)

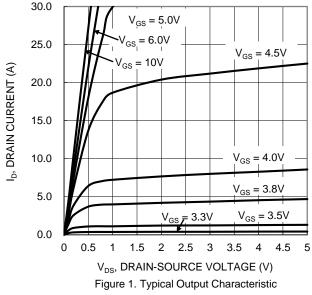
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	100	_	_	٧	$V_{GS} = 0V, I_D = 1mA$	
Zero Gate Voltage Drain Current	IDSS		_	1	μΑ	$V_{DS} = 80V$, $V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}		_	±100	nA	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	Vgs(TH)	1	_	3	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
			19	25	mΩ	VGS = 10V, ID = 20A	
Static Drain-Source On-Resistance	R _{DS(ON)}		22.1	33		$V_{GS} = 6V, I_D = 12.5A$	
		1	34	45		$V_{GS} = 4.5V, I_D = 12.5A$	
Diode Forward Voltage	VsD		0.9	1.2	V	Vgs = 0V, Is = 20A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}	1	1639			V _{DS} = 50V, V _{GS} = 0V, f = 1MHz	
Output Capacitance	Coss	_	245	_	pF		
Reverse Transfer Capacitance	Crss	_	13	_			
Gate Resistance	R_g	1	1.16	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 10V)	Qg		22.9	_		V _{DD} = 50V, I _D = 20A	
Total Gate Charge (V _{GS} = 6V)	Qg		14.5	_	nC		
Gate-Source Charge	Qgs	1	5.6	_	IIC		
Gate-Drain Charge	Q_{gd}		6.5	_			
Turn-On Delay Time	td(ON)	_	6.7	_		$V_{DD} = 50V$, $V_{GS} = 10V$, $I_{D} = 20A$, $R_{g} = 11\Omega$	
Turn-On Rise Time	t _R	_	7	_			
Turn-Off Delay Time	t _{D(OFF)}	_	15.5	_	ns		
Turn-Off Fall Time	tF	1	6.9	_			
Body Diode Reverse Recovery Time	t _{RR}	_	38.2	_	ns	Is = 20.4 di/dt = 100.4/us	
Body Diode Reverse Recovery Charge	Qrr	_	59.5	_	nC	-I _F = 20A, di/dt = 100A/μs	

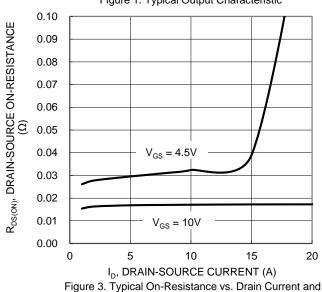
 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate. Notes:

^{7.} Short duration pulse test used to minimize self-heating effect.

^{8.} Guaranteed by design. Not subject to product testing.







0.04 DRAIN-SOURCE ON-RESISTANCE (Ω) $V_{GS} = 10V$ $T_1 = 150^{\circ}C$ 0.03 $T_{J} = 125^{\circ}C$ $T_J = 85^{\circ}C$ 0.02 $T_{.1} = 25^{\circ}C$ 0.01 $T_J = -55^{\circ}C$ R_{DS(ON)}, 0 0 5 20 30 10 15 25 I_D, DRAIN CURRENT (A)

Gate Voltage

Figure 5. Typical On-Resistance vs. Drain Current and Temperature

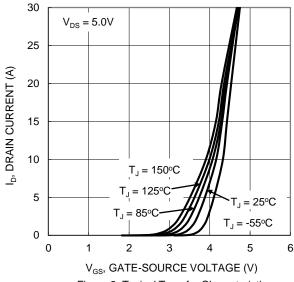


Figure 2. Typical Transfer Characteristic

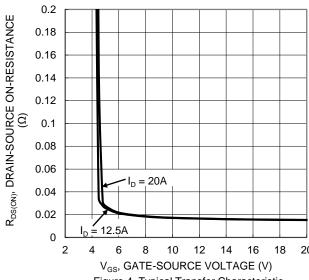


Figure 4. Typical Transfer Characteristic

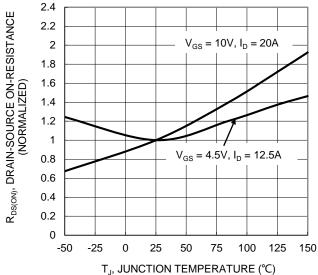
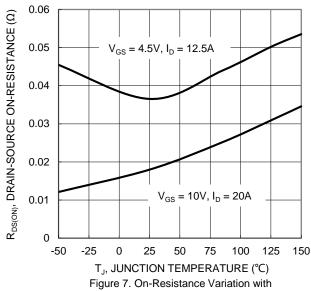


Figure 6. On-Resistance Variation with Temperature







Temperature

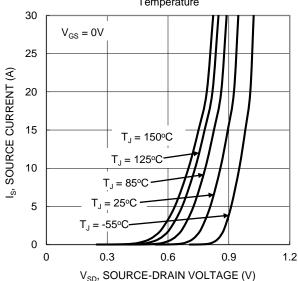


Figure 9. Diode Forward Voltage vs. Current

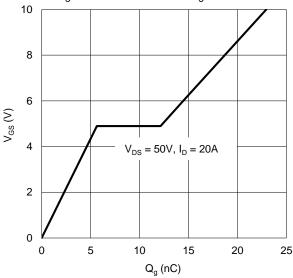


Figure 11. Gate Charge

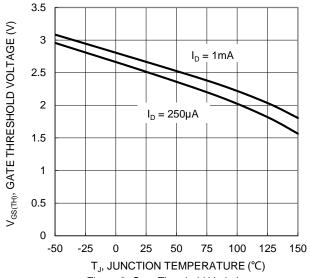


Figure 8. Gate Threshold Variation vs. Temperature

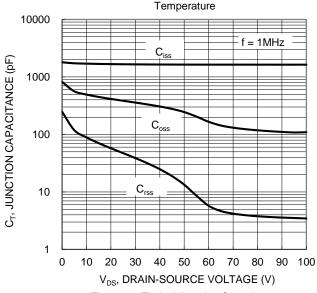


Figure 10. Typical Junction Capacitance

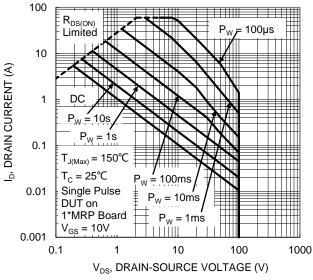


Figure 12. SOA, Safe Operation Area



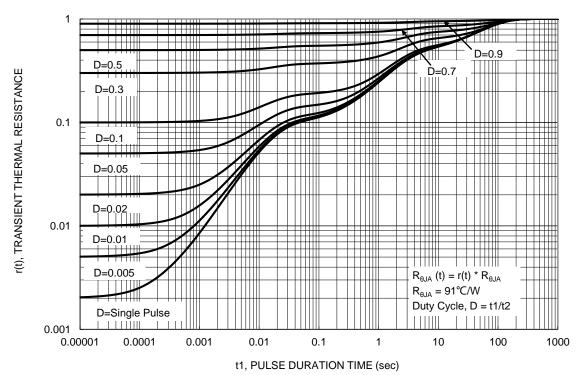


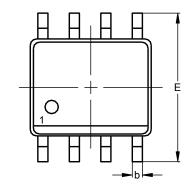
Figure 13. Transient Thermal Resistance

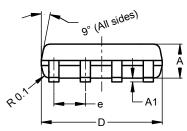


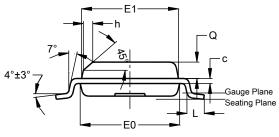
Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

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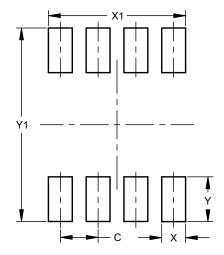


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Dim	Min	Max	Тур			
Α	1.40	1.50	1.45			
A1	0.10	0.20	0.15			
b	0.30	0.50	0.40			
С	0.15	0.25	0.20			
D	4.85	4.95	4.90			
Е	5.90	6.10	6.00			
E1	3.80	3.90	3.85			
E0	3.85	3.95	3.90			
е			1.27			
h			0.35			
L	0.62	0.82	0.72			
Q	0.60	0.70	0.65			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

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Dimensions	Value (in mm)		
С	1.27		
Х	0.802		
X1	4.612		
Υ	1.505		
Y1	6.50		



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