



ATtiny261/461/861 Automotive

Silicon Errata and Data Sheet Clarifications

Introduction

The ATtiny261/461/861 devices you have received conform functionally to the current device data sheet (www.microchip.com/mymicrochip/filehandler.aspx?ddocname=en589441), except for the anomalies described in this document. The errata described in this document will likely be addressed in future revisions of the ATtiny261/461/861 devices.

Note:

- This document summarizes all the silicon errata issues from all the silicon revisions, previous and current.

1. Silicon Issue Summary

Legend

- Erratum is not applicable.
- X Erratum is applicable.

Peripheral	Short Description	Valid for Silicon Revision		
		ATtiny261	ATtiny461	ATtiny861
		Rev. D ⁽¹⁾	Rev. C ⁽¹⁾	Rev. D ⁽¹⁾
Analog comparator	2.1.1.1. Trigger Reference Levels of the Analog Comparator	X	X	X

Note: 1. This revision is the initial release of the silicon.

2. Silicon Errata Issues

2.1 Errata Details

- Erratum is not applicable.
- X Erratum is applicable.

2.1.1 Analog Comparator

2.1.1.1 Trigger Reference Levels of the Analog Comparator

The analog comparator will not use trigger reference levels less than 300 mV with respect to GND.

Work around

None.

Affected Silicon Revisions

ATtiny261	ATtiny461	ATtiny861
Rev. D	Rev. C	Rev. D
X	X	X

3. Data Sheet Clarifications

The following typographic corrections and clarifications are to be noted for the latest version of the device data sheet (www.microchip.com/mymicrochip/filehandler.aspx?ddocname=en589441).

Note: Corrections are shown in **bold**. Where possible, the original bold text formatting has been removed for clarity.

3.1 Errata Section in Data Sheet is no Longer Valid

A clarification for the Errata section in the device data sheet has been made.

The errata content has been moved to a separate document, *ATtiny261/461/861 Automotive Silicon Errata and Data Sheet Clarifications* (this document).

For the latest errata, see the *Silicon Errata Issues* section of this document.

3.2 Package Marking Information

Legend:	XX...X	Customer-specific information or Microchip part number
	Y	Year code (last digit of calendar year)
	YY	Year code (last 2 digits of calendar year)
	WW	Week code (week of January 1 is week '01')
	NNN	Alphanumeric traceability code
	(e3)	Pb-free JEDEC® designator for Matte Tin (Sn)
Note:	In the event the full Microchip part number cannot be marked on one line, it will be carried over to the next line, thus limiting the number of available characters for customer-specific information.	

3.2.1 20-Pin SOIC

Figure 3-1. General

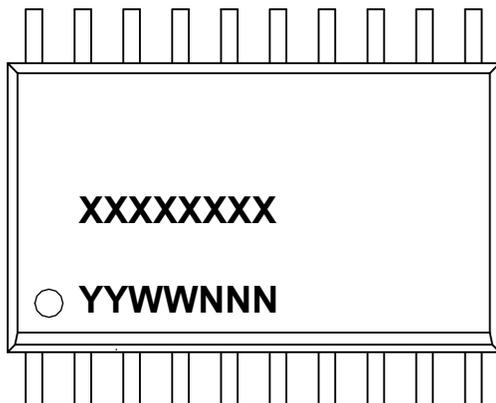
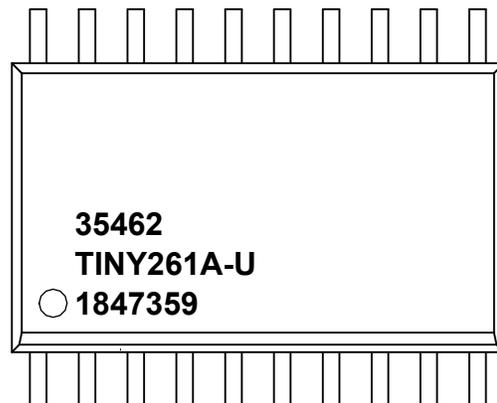


Figure 3-2. Example



3.2.2 20-Pin TSSOP

Figure 3-3. General

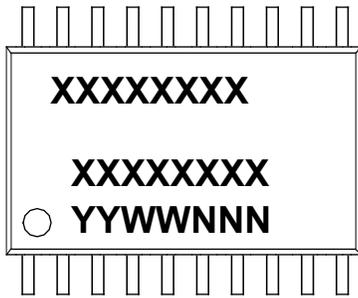
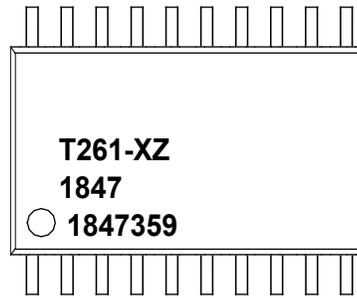


Figure 3-4. Example



3.2.3 20-Pin QFN

Figure 3-5. General

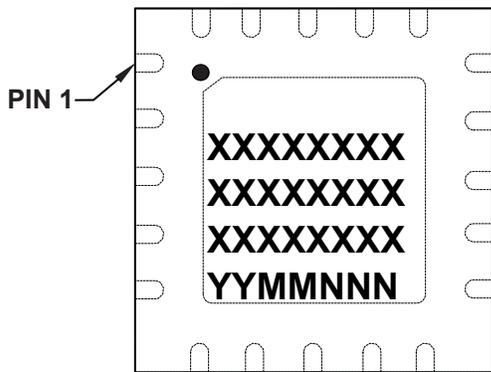
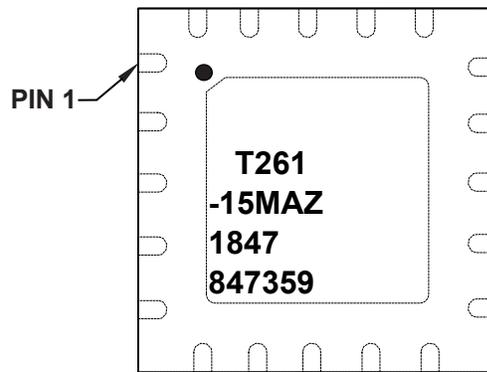


Figure 3-6. Example



3.2.4 32-Pin QFN

Figure 3-7. General

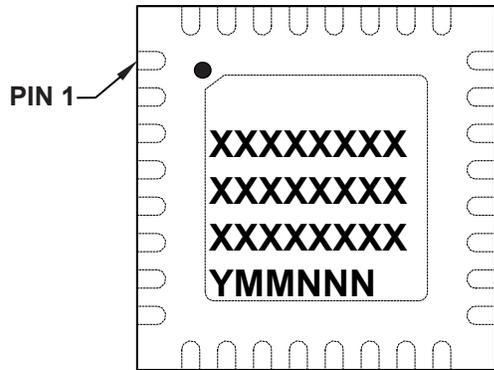
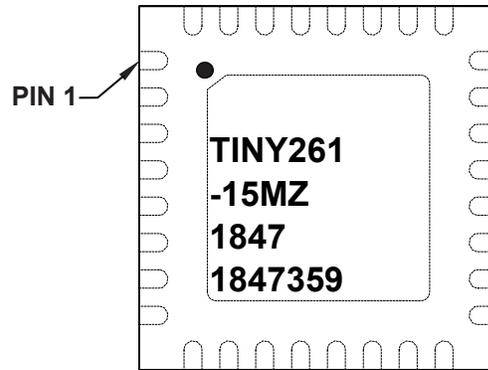


Figure 3-8. Example



4. Document Revision History

Note: The document revision is independent of the silicon revision.

4.1 Revision History

Doc Rev.	Date	Comments
A	01/2022	Initial release of this document. <ul style="list-style-type: none">• Content moved from the data sheet and restructured to the new document template• Updated the die revision list to reflect die revisions in production.• Added 3.1 and 3.2 Data Sheet Clarifications sections.

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