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Vishay Dale

Power Metal Strip[®] Battery Shunt Resistor, Very Low Value (100 $\mu\Omega$)



LINKS TO ADDITIONAL RESOURCES



FEATURES

- High power to resistor size ratio
- Proprietary processing technique produces extremely low resistance values
- All welded construction
- Solid metal manganese-copper alloy resistive element with low TCR (< 20 ppm/°C)
- Very low inductance (< 5 nH)
- Low thermal EMF (< 1 μV/°C)
- AEC-Q200 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>



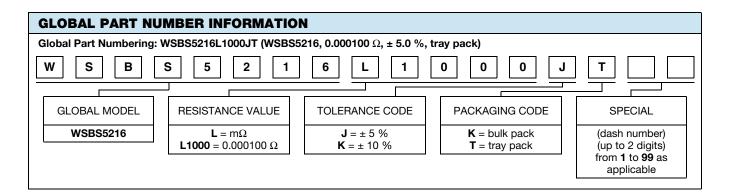
(5-2008)

STANDARD ELECTRICAL SPECIFICATIONS								
GLOBAL MODEL	SIZE	POWER RATING P _{70 °C} W	TOLERANCE ± %	$\begin{array}{c} \textbf{RESISTANCE VALUE} \\ \textbf{RANGE} \\ \Omega \end{array}$	RESISTANCE VALUES CURRENTLY AVAILABLE (1) Ω	WEIGHT (typical) g		
WSBS5216	5216	12	5, 10	50μ to 250μ	100μ	19.2		

Note

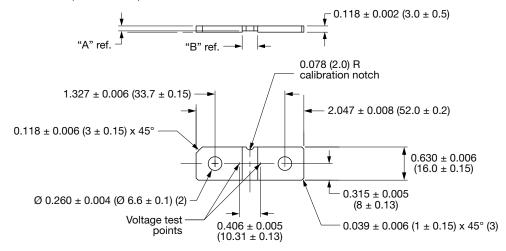
⁽¹⁾ Other values may be available, contact factory

TECHNICAL SPECIFICATIONS						
PARAMETER	UNIT	RESISTOR CHARACTERISTICS				
Temperature coefficient	ppm/°C	± 150				
Temperature coefficient (element material)	ppm/°C	± 20				
Operating temperature range	°C	-65 to +170				
Thermal EMF	μV/°C	< 1 for 100 μΩ				
Inductance	nH	< 5				
Maximum continuous current rating	А	(P/R) ^{1/2}				

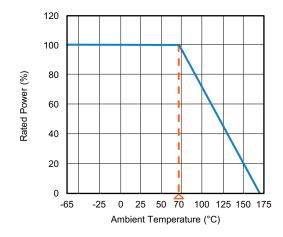




DIMENSIONS in inches (millimeters)



DERATING



TOLERANCES ON DECIMALS XXX ± 0.005	
UNLESS OTHERWISE LISTED	

RESISTANCE	ELEMENT	
VALUE (μΩ)	MATERIAL	
100	Mn-Cu	

PERFORMANCE					
TEST	CONDITIONS OF TEST	TEST LIMITS			
Thermal shock	-55 °C to +150 °C, 1000 cycles, 15 min at each extreme	± 0.5 % ΔR			
Short time overload	5 x rated power for 5 s	± 0.5 % ΔR			
Low temperature storage	-65 °C for 24 h	± 0.5 % ΔR			
High temperature exposure	1000 h at +170 °C	± 1.0 % ΔR			
Bias humidity	+85 °C, 85 % RH, 10 % bias, 1000 h	± 0.5 % ΔR			
Mechanical shock	100 g's for 6 ms, 5 pulses	± 0.5 % ΔR			
Vibration	Frequency varied 10 Hz to 2000 Hz in 1 min, 3 directions, 12 h	± 0.5 % ΔR			
Load life	1000 h at +70 °C, 1.5 h "ON", 0.5 h "OFF"	± 1.0 % ΔR			
Moisture resistance	MIL-STD-202, method 106, 0 % power, 7b not required	± 0.5 % ΔR			



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