

**General Description**

The AOTF5N50FD has been fabricated using an advanced high voltage MOSFET process that is designed to deliver high levels of performance and robustness in popular AC-DC applications. By providing low  $R_{DS(on)}$ ,  $C_{iss}$  and  $C_{rss}$  along with guaranteed avalanche capability this part can be adopted quickly into new and existing offline power supply designs.

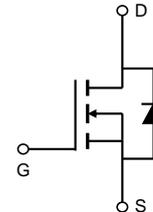
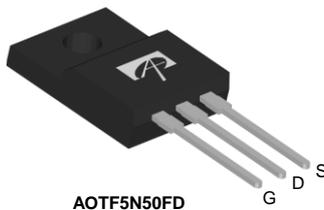
**Product Summary**

$V_{DS}$	600V@150°C
$I_D$ (at $V_{GS}=10V$ )	5A
$R_{DS(ON)}$ (at $V_{GS}=10V$ )	<1.8Ω

100% UIS Tested  
 100%  $R_g$  Tested



Top View  
 TO-220F


**Absolute Maximum Ratings  $T_A=25^\circ\text{C}$  unless otherwise noted**

Parameter	Symbol	AOTF5N50FD	Units
Drain-Source Voltage	$V_{DS}$	500	V
Gate-Source Voltage	$V_{GS}$	$\pm 30$	V
Continuous Drain Current	$I_D$	$T_C=25^\circ\text{C}$	5*
		$T_C=100^\circ\text{C}$	3*
Pulsed Drain Current <sup>c</sup>	$I_{DM}$	13	A
Avalanche Current <sup>c</sup>	$I_{AR}$	2.3	A
Repetitive avalanche energy <sup>c</sup>	$E_{AR}$	79	mJ
Single pulsed avalanche energy <sup>g</sup>	$E_{AS}$	158	mJ
Peak diode recovery dv/dt	dv/dt	5	V/ns
Power Dissipation <sup>b</sup>	$P_D$	$T_C=25^\circ\text{C}$	35
		Derate above $25^\circ\text{C}$	0.3
Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	$^\circ\text{C}$
Maximum lead temperature for soldering purpose, 1/8" from case for 5 seconds	$T_L$	300	$^\circ\text{C}$

**Thermal Characteristics**

Parameter	Symbol	AOTF5N50FD	Units
Maximum Junction-to-Ambient <sup>A,D</sup>	$R_{\theta JA}$	65	$^\circ\text{C/W}$
Maximum Junction-to-Case	$R_{\theta JC}$	3.6	$^\circ\text{C/W}$

\* Drain current limited by maximum junction temperature.

**Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise noted)**

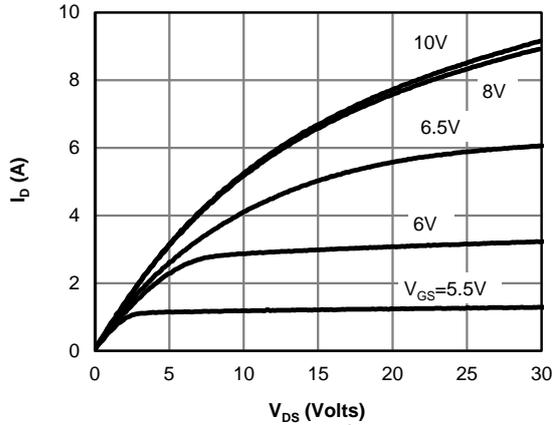
Symbol	Parameter	Conditions	Min	Typ	Max	Units
<b>STATIC PARAMETERS</b>						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	I <sub>D</sub> =10mA, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C I <sub>D</sub> =10mA, V <sub>GS</sub> =0V, T <sub>J</sub> =150°C	500		600	V
BV <sub>DSS</sub> /ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> =10mA, V <sub>GS</sub> =0V		0.56		V/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =500V, V <sub>GS</sub> =0V V <sub>DS</sub> =400V, T <sub>J</sub> =125°C			10 100	μA
I <sub>GSS</sub>	Gate-Body leakage current	V <sub>DS</sub> =0V, V <sub>GS</sub> =±30V			±100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =5V, I <sub>D</sub> =250μA	2.5	3.5	4.2	V
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =2.5A		1.5	1.8	Ω
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> =40V, I <sub>D</sub> =2.5A		4		S
V <sub>SD</sub>	Diode Forward Voltage	I <sub>S</sub> =5A, V <sub>GS</sub> =0V		0.93	1.6	V
I <sub>S</sub>	Maximum Body-Diode Continuous Current				5	A
I <sub>SM</sub>	Maximum Body-Diode Pulsed Current				13	A
<b>DYNAMIC PARAMETERS</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V, f=1MHz	350	440	530	pF
C <sub>oss</sub>	Output Capacitance		35	50	65	pF
C <sub>riss</sub>	Reverse Transfer Capacitance		2.5	4.5	6.5	pF
R <sub>g</sub>	Gate resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz	1.7	3.4	5.2	Ω
<b>SWITCHING PARAMETERS</b>						
Q <sub>g</sub>	Total Gate Charge	V <sub>GS</sub> =10V, V <sub>DS</sub> =400V, I <sub>D</sub> =5A	8	11	15	nC
Q <sub>gs</sub>	Gate Source Charge			2.7		nC
Q <sub>gd</sub>	Gate Drain Charge			3.8		nC
t <sub>D(on)</sub>	Turn-On DelayTime	V <sub>GS</sub> =10V, V <sub>DS</sub> =250V, I <sub>D</sub> =5A, R <sub>G</sub> =25Ω		18		ns
t <sub>r</sub>	Turn-On Rise Time			33		ns
t <sub>D(off)</sub>	Turn-Off DelayTime			31		ns
t <sub>f</sub>	Turn-Off Fall Time			26		ns
t <sub>rr</sub>	Body Diode Reverse Recovery Time	I <sub>F</sub> =5A, di/dt=100A/μs, V <sub>DS</sub> =100V		87	145	ns
Q <sub>rr</sub>	Body Diode Reverse Recovery Charge	I <sub>F</sub> =5A, di/dt=100A/μs, V <sub>DS</sub> =100V		0.2	0.4	μC

- A. The value of R<sub>θJA</sub> is measured with the device in a still air environment with T<sub>A</sub>=25° C.
- B. The power dissipation P<sub>D</sub> is based on T<sub>J(MAX)</sub>=150° C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
- C. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150° C, Ratings are based on low frequency and duty cycles to keep initial T<sub>J</sub>=25° C.
- D. The R<sub>θJA</sub> is the sum of the thermal impedance from junction to case R<sub>θJC</sub> and case to ambient.
- E. The static characteristics in Figures 1 to 6 are obtained using <300 μs pulses, duty cycle 0.5% max.
- F. These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of T<sub>J(MAX)</sub>=150° C. The SOA curve provides a single pulse rating.
- G. L=60mH, I<sub>AS</sub>=2.3A, V<sub>DD</sub>=150V, R<sub>G</sub>=25Ω, Starting T<sub>J</sub>=25° C

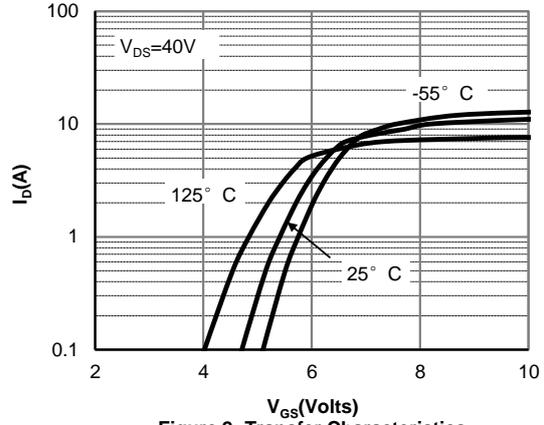
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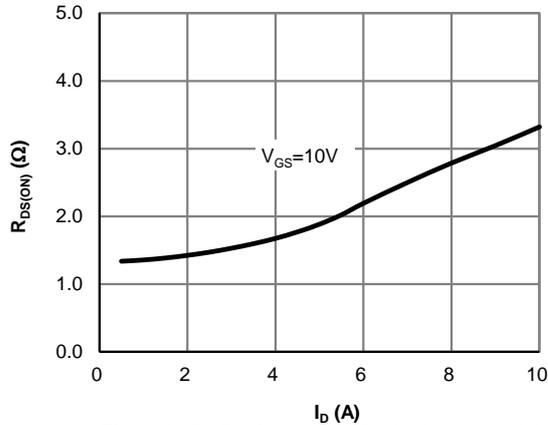
**TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS**



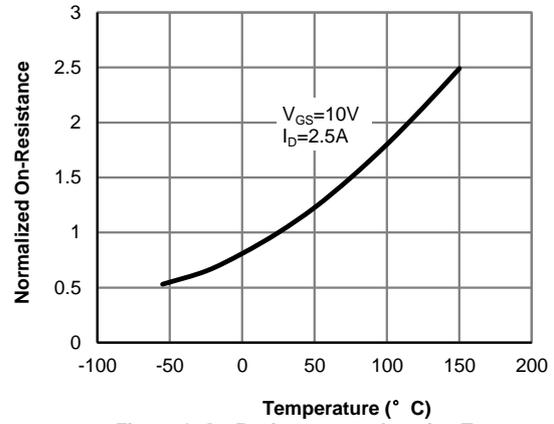
**Figure 1: On-Region Characteristics**



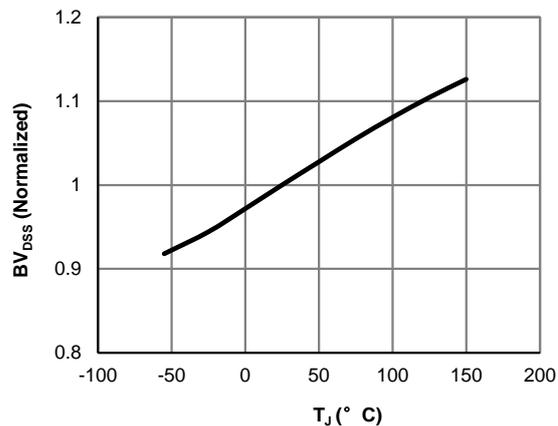
**Figure 2: Transfer Characteristics**



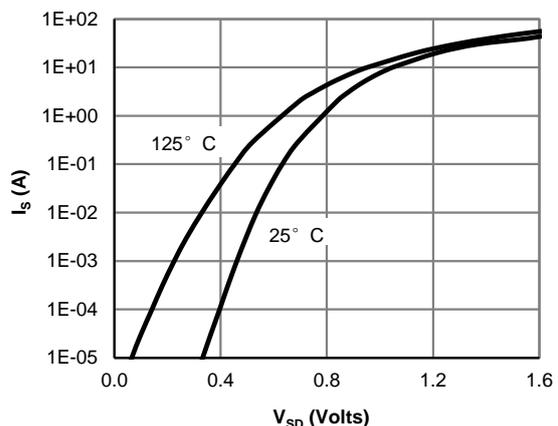
**Figure 3: On-Resistance vs. Drain Current and Gate Voltage**



**Figure 4: On-Resistance vs. Junction Temperature**

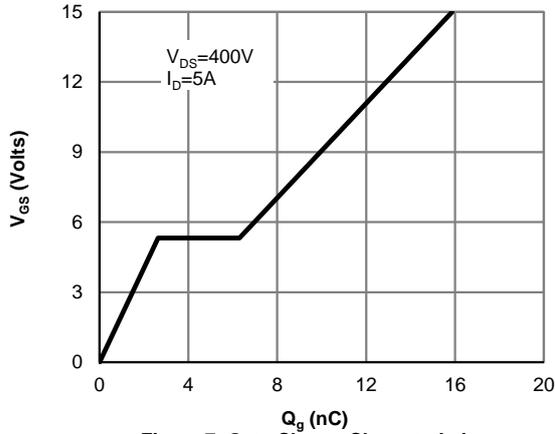


**Figure 5: Break Down vs. Junction Temperature**

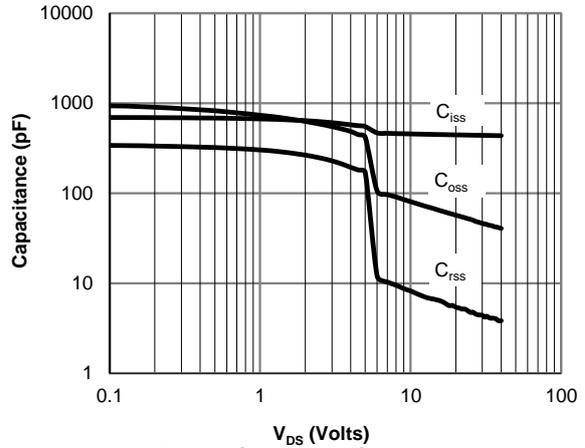


**Figure 6: Body-Diode Characteristics (Note E)**

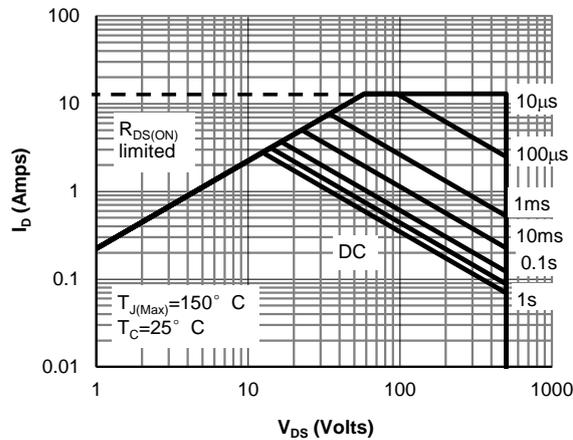
**TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS**



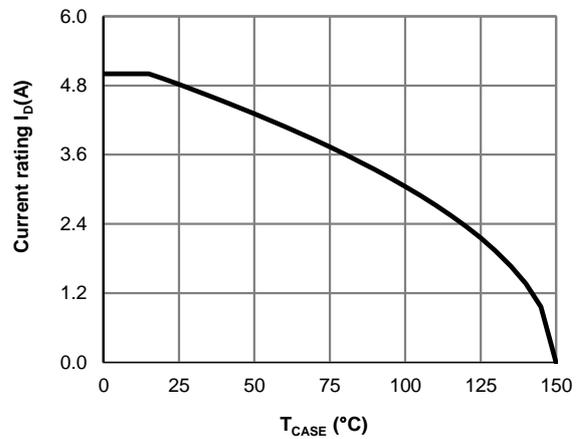
**Figure 7: Gate-Charge Characteristics**



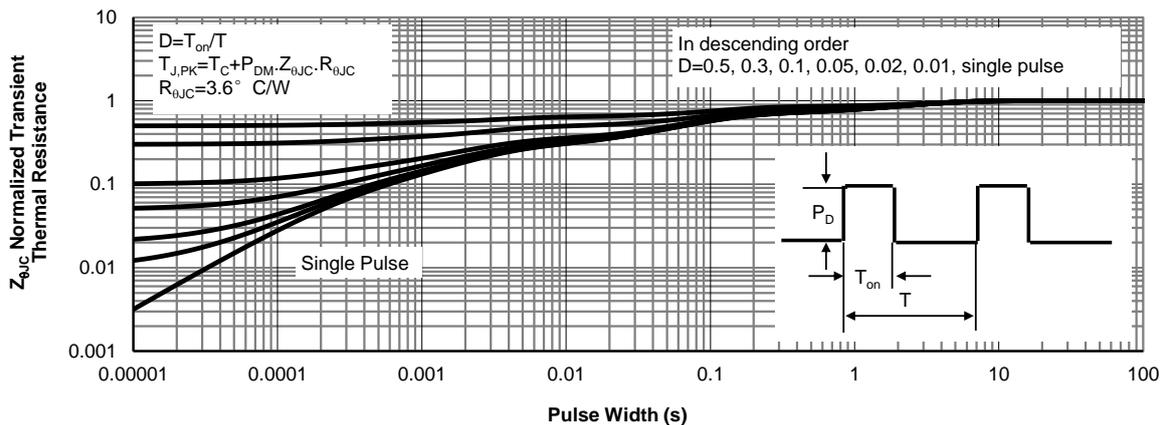
**Figure 8: Capacitance Characteristics**



**Figure 9: Maximum Forward Biased Safe Operating Area for AOTF5N50FD (Note F)**

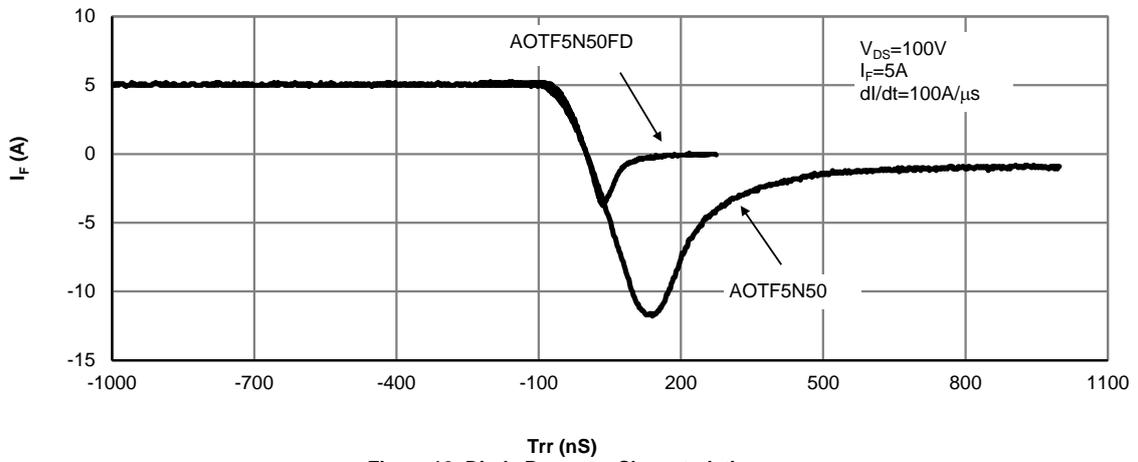


**Figure 10: Current De-rating (Note B)**



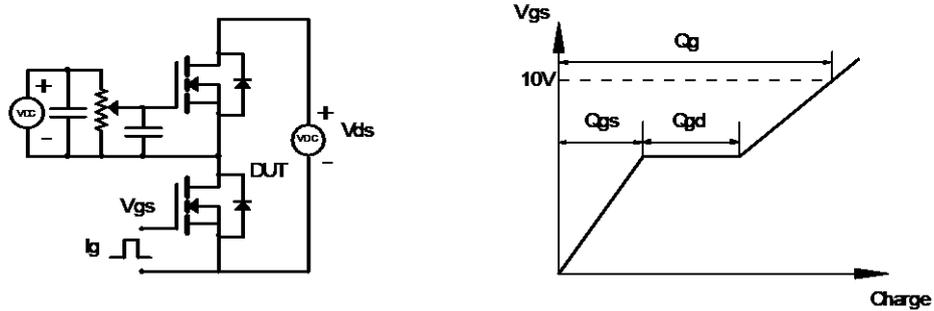
**Figure 11: Normalized Maximum Transient Thermal Impedance for AOTF5N50FD (Note F)**

**TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS**

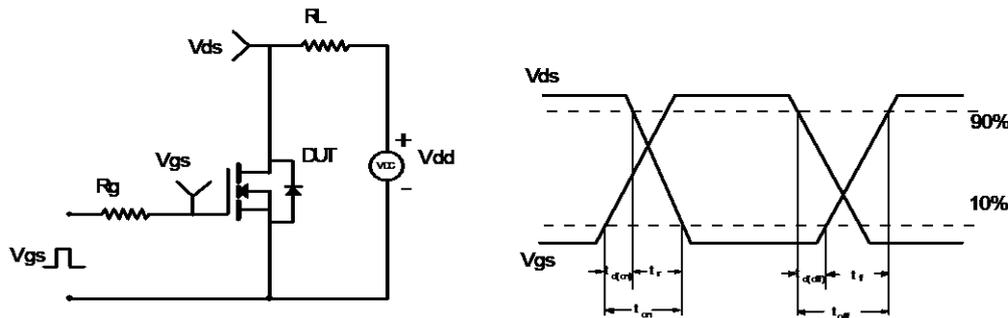


**Figure 12: Diode Recovery Characteristics**

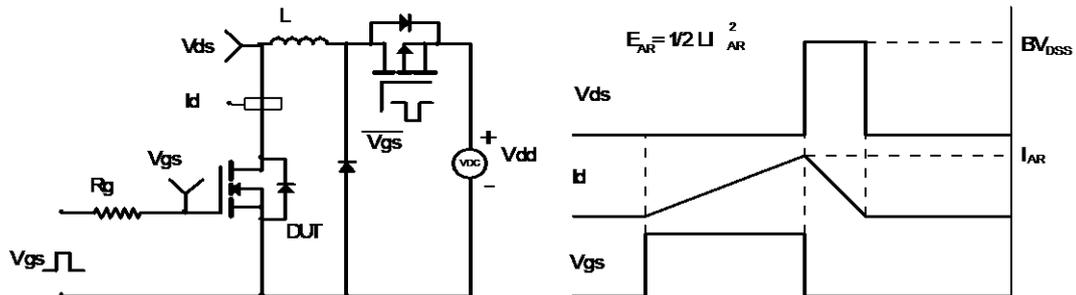
**Gate Charge Test Circuit & Waveform**



**Resistive Switching Test Circuit & Waveforms**



**Unclamped Inductive Switching (UIS) Test Circuit & Waveforms**



**Diode Recovery Test Circuit & Waveforms**

